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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/646,721	08/25/2003	Hoang Nhu	1875.4820001	9844
	7590 12/29/2006 SLER, GOLDSTEIN & FO	EXAMINER		
1100 NEW YO	RK AVENUE, N.W.		YENKE, BRIAN P	
WASHINGTO	N, DC 20005		ART UNIT	PAPER NUMBER
			2622	
		•		
SHORTENED STATUTOR	Y PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE	
3 MO	NTHS	12/29/2006	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

		Application No.	Applicant(s)				
Office Action Commercial		10/646,721	NHU, HOANG				
	Office Action Summary	Examiner	Art Unit	•			
		BRIAN P. YENKE	2622				
Period fo	The MAILING DATE of this communication a or Reply	appears on the cover sheet with the	he correspondence ad	dress			
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REF CHEVER IS LONGER, FROM THE MAILING nsions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory pen ire to reply within the set or extended period for reply will, by star reply received by the Office later than three months after the may ed patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICAT 1.136(a). In no event, however, may a reply bod will apply and will expire SIX (6) MONTHS tute, cause the application to become ABAND	YON. be timely filed from the mailing date of this or ONED (35 U.S.C. § 133).				
Status							
1)⊠	Responsive to communication(s) filed on Arr	nendment (20 Oct 06)					
2a)⊠		his action is non-final.					
3)	/ _						
٥,۵	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disnositi	ion of Claims	. En parto Quejro, rocc cier ri	, 100 0.0. 2.0.				
		e e					
	Claim(s) 1,2 and 4-15 is/are pending in the a						
	4a) Of the above claim(s) is/are withd	rawn from consideration.					
·	Claim(s) is/are allowed.						
	Claim(s) <u>1,2 and 4-15</u> is/are rejected.		•				
7)∐	Claim(s) is/are objected to.						
8)	Claim(s) are subject to restriction and	d/or election requirement.					
Applicati	ion Papers						
9)[The specification is objected to by the Exami	iner.					
10)🛛	10)⊠ The drawing(s) filed on <u>20 October 2006</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.						
	Applicant may not request that any objection to the						
	Replacement drawing sheet(s) including the corr			FR 1.121(d).			
11)	The oath or declaration is objected to by the		•				
Priority ι	ınder 35 U.S.C. § 119						
12)	Acknowledgment is made of a claim for forei	an priority under 35 U.S.C. § 119	9(a)-(d) or (f).				
	☐ All b)☐ Some * c)☐ None of:		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,				
	1. Certified copies of the priority docume	ents have been received.					
	2. Certified copies of the priority docume		cation No.				
	3. Copies of the certified copies of the pr	• •		Stage			
	application from the International Bure			o.ago			
* 5	See the attached detailed Office action for a li	· · · · · · · · · · · · · · · · · · ·	eived.				
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Attachmen							
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948)	4) LInterview Summ Paper No(s)/Ma					
	mation Disclosure Statement(s) (PTO/SB/08)	5) Notice of Inform					
	r No(s)/Mail Date	6) Other:					

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DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed 20 Oct 06 have been fully considered but they are not persuasive.

Applicant's Arguments

a) Applicant states that Vierthaler does not disclose a BTSC decoder including a DSP configured to decode digital samples via a digital interface, wherein the digital samples are composite BTSC samples formatted in accordance with MTS standards.

Examiner's Response

a) The examiner disagrees. Vierthaler discloses a digital BTSC decoder 28 (Fig 1, col 4, line 33), Vierthaler also discloses that since BTSC decoder 28 is a digital component as well as the digital frequency modulation demodulator which provides decoder 28 a digital signal, exact level ratios exist between them, independently of external wiring, thus they can be made as an integrated circuit (col 3, line 49 - col 4, line 4). Thus Vierthaler clearly discloses a system which processes digital signals via decoder 28 (meeting a DSP). Vierhaler provides in the background that the BTSC is a standard which where stereo sound in television and video is transmitted and processed, this standard is also known as the Multichannel Television Sound (MTS) (col 1, line 1-25). According to the standard the sum R+L and the difference L-R of the signal R of the right stereo channel and the signal L of the left stereo channel are formed at the transmitter. The sum and difference signal are the composite BTSC samples/signals.

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Drawings

2. The drawings filed on 20 Oct 06 are approved by the examiner.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-2 and 4-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Vierthaler et al., US 6,281,813.

In considering claims 1-2, 8, 11 and 14-15,

- a) the claimed intermediate frequency demodulator... is met by DFD 26, which receives the sound intermediate frequency signal 34 from tuner 20 (Fig 1).
- b) the claimed DSP...is met by digital BTSC decoder 28 (Fig 1).
- c) the claimed an all digital interface... is met where the output from DFD 26 to DD 28 (the interface) is a digital MTS signal 36.

In considering claims 4 and 12,

As shown in Fig 1, the system does not include a DAC (Fig 1).

In considering claims 5, 7, 9

As shown in Fig 1, the system does not include a gain control device (Fig 1).

In considering claims 6 and 13,

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As shown in Fig 1, the interface does not include an ADC, since the ADC is included prior to the interface.

In considering claim 10,

The output of the system provides a digital output signal 3 which are then separated into right and left audio signal (all being digital), where the signals are able to be scaled (scaleable).

Conclusion

4. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian Yenke whose telephone number is (571)272-7359. The examiner work schedule is Monday-Thursday, 0730-1830 hrs.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's Supervisor, David L. Ometz, can be reached at (571)272-7593.

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Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(571)-273-8300

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is

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B.P.*Y* 24 Dec 06

> BRIAN P. YENKE PRIMARY EXAMINEF